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PATENT  
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**IN THE SPECIFICATION**

Please replace the paragraph on page 7, line 1 with:

FIGURES 10a-10d ~~FIGURES 10a-10b~~ are block diagrams illustrating various modes of operation of a blazed grating based 2x2 optical switch constructed according to the teachings of the present invention;

Please amend page 46 of the present patent application by deleting the following:

- ~~32. — An optical processing device, comprising:~~  
~~a separator operable to separate an input optical signal into one or more optical signal wavelengths; and~~  
~~a linear array of variable blazed gratings located on one or more semiconductor substrates, each of the variable blazed gratings operable to perform an optical signal processing operation on at least one optical signal wavelength, the optical signal processing operation based at least in part on a control signal received from a controller.~~
- ~~33. — The optical processing device of Claim 32, wherein the optical processing device performs a function selected from the group consisting of variable attenuation, an optical add/drop multiplexing, and an optical routing.~~
- ~~34. — The optical processing device of Claim 32, wherein the separator is located on the semiconductor substrate.~~
- ~~35. — The optical processing device of Claim 32, wherein at least one of the variable blazed gratings comprises:~~  
~~an inner conductive layer; and~~  
~~a plurality of approximately adjacent at least partially reflective mirror strips disposed outwardly from the inner conductive layer, each strip operable to receive at least a~~

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~~portion of the input optical signal, wherein each of the plurality of strips is operable to undergo a partial rotation.~~

~~36. The optical processing device of Claim 35, wherein none of the strips has a width greater than 40 microns.~~

~~37. The optical processing device of Claim 35, wherein the strips are operable to undergo a maximum rotation that is greater than 2 degrees.~~

~~38. The optical processing device of Claim 32, wherein the one or more semiconductor substrates comprise silicon.~~